

a plurality of selecting thin film transistors connected to the plurality of first signal lines, respectively;

a plurality of logic circuits to output selection signals to the plurality of selecting thin film transistors, respectively;

a shift register connected to a first terminal of each of the plurality of logic circuits; and

a mask signal generation circuit to output a mask signal to a second terminal of the plurality of logic display.

40. The active matrix type display device according to claim 39 wherein said display device is an EL display.

41. The active matrix type display device according to claim 39 wherein said display device is an organic EL display.

42. The active matrix type display device according to claim 39 wherein said display device is an inorganic EL display.

43. The active matrix type display device according to claim 39 wherein said display device is a liquid crystal device.

44. The active matrix type display device according to claim 39 further comprising field emission diodes in the picture elements.--

REMARKS

This Preliminary Amendment is being submitted together with a *Request for Continued Examination* to add new claims 33-44. Examination of these new claims on the merits is respectfully requested.

The present application includes priority claims to two Japanese applications, JP 6-267244 and JP 6-267242. The priority claim to application JP 6-267242 was filed on

December 12, 2000. Based on the currently claimed subject matter, it appears the claim to this application may no longer be appropriate. Careful consideration of this matter and correction of the Patent Office records as appropriate is respectfully requested.

The present application is believed to be in condition for allowance and favorable consideration is requested in the RCE application. If the Examiner feels further discussions would expedite prosecution of this application, he is invited to contact the undersigned.

Respectfully submitted,


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